Publications

- Books/Book Chapters

- Journals
  8. D. Liu, X. Ni, R. Zhou, W. Rhee, and Z. Wang, “A 0.42 mW 1 Mb/s 3-to-4 GHz transceiver in 0.18 µm CMOS with flexible efficiency, bandwidth and distance


52. D. Wilson, W. Rhee, and B. S. Song, “Integrated RF receiver front ends and frequency synthesizers for wireless,” Emerging Technologies: Designing Low
Conferences

<Best Student Paper Award>


<Best Paper Award>


□ Tutorials & Invited Talks (International)


13. W. Rhee, “Phase-locked clock generation for SoC: Circuit and system design aspects,” IEEE International System-on-Chip Conference (SOCC), Beijing, China, September 2015.

- Patents
  23 U.S. patents issued.

- Ph.D. Thesis
  Multi-bit delta-sigma modulation technique for fractional-N frequency synthesizers,