Publications

- **Books/Book Chapters**

- **Journals**


- Conferences


<Best Paper Award>
32. S. Geng, D. Liu, Y. Li, H. Zhuo, W. Rhee, and Z. Wang, “A 13.3mW 500Mb/s IR-UWB transceiver with link margin enhancement technique for meter-range


- **Tutorials & Invited Talks (International)**
  7. W. Rhee, “Phase-locked clock generation for SoC: Circuit and system design aspects,” *IEEE International System-on-Chip Conference (SOCC)*, Beijing, China, September 2015.

- **Patents**
  23 U.S. patents issued.

- **Ph.D. Thesis**